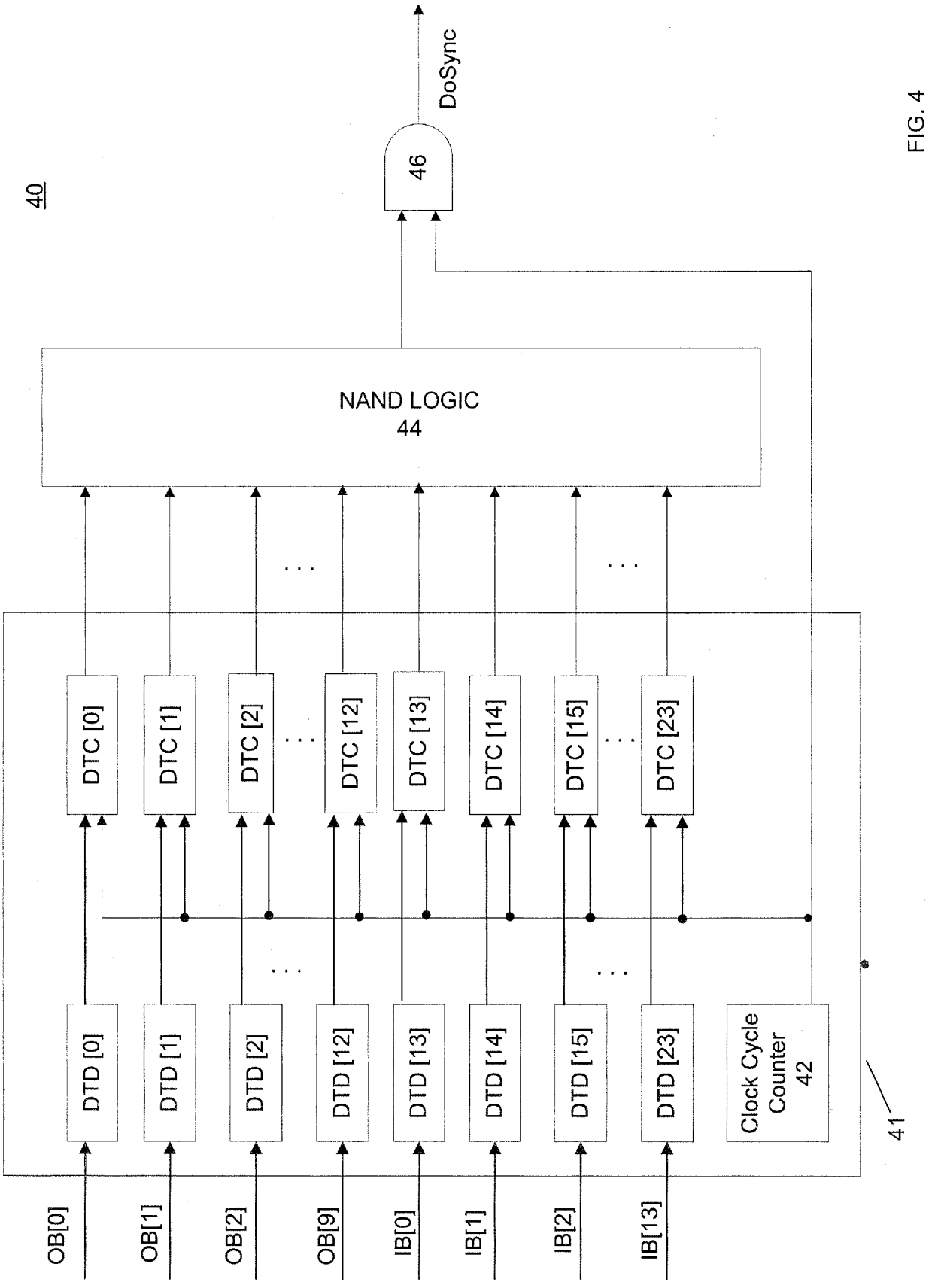

40
46
DoSync
NAND LOGIC
44
DTC [0]
DTC [1]
DTC [2]
DTC [12]
DTC [13]
DTC [14]
DTC [15]
DTC [23]
DTD [0]
DTD [1]
DTD [2]
DTD [12]
DTD [13]
DTD [14]
DTD [15]
DTD [23]
Clock Cycle Counter 42
41
OB[0]
OB[1]
OB[2]
OB[9]
IB[0]
IB[1]
IB[2]
IB[13]

FIG. 4